

Arkham Technology®

ASIC/FPGA Cores & Software Libraries

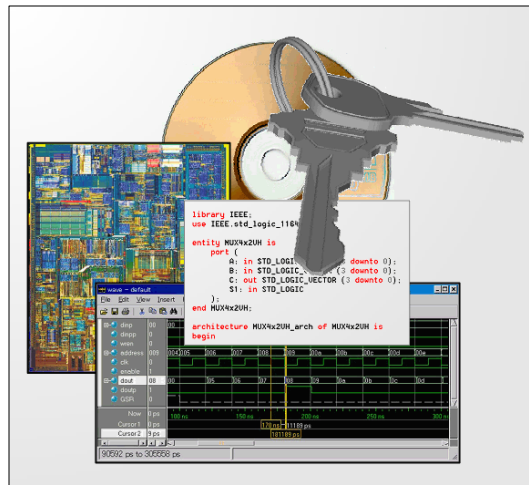
SPECIFICATIONS:

Developed using IEEE
1076 Standard VHDL

Available as targeted
netlists or target
independent source code
depending on licensing
scope

CUSTOM DESIGN SERVICE AVAILABLE

- Classified processing available
- DMEA Trusted Foundry Certified



- **Key Management**
 - DS-101 ECU IP Core for ASIC/FPGA
 - High-level 87-27 Issue/Fill support
 - DS-101 CODEC for ASIC/FPGA
 - Converts UARTs to DS-101 Physical I/F
 - DS-101 Software Library
 - High-level 87-27 Issue/Fill/CT3 support
 - KMI OTNK Software Library
- **Crypto Cores for ASIC/FPGA**
 - AES Basic (CTR, CFB, OFB, CBC)
 - AES GCM
 - AES Data At Rest (XTS)
 - CNSA ECU Security Management
 - WATARI, Key Wrap, AES-256, SHA-384
 - Suite A (Contact us)
 - IASRD-Compliant Randomizer
- **Custom Design Services**
 - Experienced Type 1 FPGA/ASIC & Software Developers
 - Certified-Module Embedders (CMEs) & Certification Support

Typical Application

